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Referred To As A Self-biased Current Source). $R = V_{DD} / GND$ M_0 M_2 M_1 M_3 GND I_{Out} Assume All Transistors Are Operating In Saturation Region And $V_{TH} = 0$, $V_{DD} = 3V$, $V_{TH}(NMOS) = 0$... Mar 2th, 2024EE 338L: Analog Integrated Circuit Design Fall 2012Output Stages; Analog Filters. CAD Tools For Circuit Analysis And Design. Textbook Behzad Razavi,
Fundamental Of Microelectronics, Wiley, 2008. Other Helpful References: Paul Gray Et Al., Analysis And Design Of Analog Integrated Circuits, Wiley, 2009. Behzad Razavi, Design Of
Analog CMOS Integrated Circuits, McGraw-Hill, 2000. Mar 1th, 2024.
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