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In Addition, A Ferrite Bead Should Be Used To Isolate Each Analog Supply From The Rest Of The Board. The Bead Should Be Placed In Series ... Steward HI 1206N101R-00 Or Similar. Note:Place Vias Close To Or Under Neath The Capacitor Pads For Low Inductive Path And Higher Heat Dissipation Rate. 0.1 UF. A B C 13th, 2024

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Figure 3-2 Shows The Best Placement Method, Using An Analog-to-digital Converter (ADC). One Side Of The A/D Has Analog Pins Resting Directly On Top Of The Analog Plane, While The Other Side's Digital Pins Are Squarely On The Digital Plane. This Configuration Is Ideal But The Evaluation Board Is More A 1th, 2024

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- Select The Cc Layer From The LSW.
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