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5. Table 1 Shows The Device Utilization Summary For Adder/subtractor And Table 2 Shows Device Utilization Summary For Multiplier. The Parameters Such As Number Of Slices Registers, Number Feb 1th, 2024 ECE-342 Lab 5: BJT Amplifier Sample Lab Report The Corresponding Small-signal Model Of The Circuit Is Also Shown. The Model Is Based On An Assumptions That The Capacitor Impedance Is Small At The Input Signal Frequencies (and May Be Considered A Short), And The Transistor Output Resistance R_{Ois} Is Large Compared To R_L , And May Be Neglected. The Small Signal Parameters Are $G_M = I_{C3} V_T / (1 + R \dots$ Jul 1th, 2024.

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