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VLSI Lab Manual VII Sem, ECE - Gopalan Colleges 3. Write The Verilog Program For Your Design (e.g.: Codefile1.v) Gedit Codefile1.v 4. Write The Verilog Test Bench Program For Your Design (e.g.: Codefile1_tb.v). Now, The Design Entry Using HDL Gets Finished. Gedit Codefile1_tb.v II. STEPS FOR SIMULATION: 1. Initially, Both Of Your Verilog Programs Have To Be Compiled 2. Feb 11th, 2024 Computer Programming Laboratory - Gopalan Colleges PALINDROME AIM: To Check Whether The Given Integer Is PALINDROME Or NOT. ALGORITHM : ALGM: Palindrome [This Algorithm Takes An Integer Number As Input And Output The Reverse Of The Same. Also Checks The Number Is Palindrome Or Not] Steps: 1. [Initialize] Start 2. [Input The Original Number] Read Num 3. [Set Number Num To A Variable N] $N \leftarrow \text{Num}$ 4. Feb 7th, 2024 Syllabus For Unit Test -1, Std. IX - Gopalan School GEOGRAPHY HISTORY & CIVICS PHYSICS CHEMISTRY BIOLOGY 6th SUBJECT COMPUTER SCIENCE Th 6 SUBJECT ECONOMICS SYLLABUS UNIT TEST -1 -IX STD Drama: Act I, Scene 1 POEM: CFI-I & 2 S. Story: CH-I As Per ICSE Format Sahitya Sagar — CH-1,2,3 Ekanki Sanchay — CH-I SAHITYA Jan 9th, 2024.

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