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MIPI-CSI2 Peripheral On I.MX6 MPUs For More Details, See Section 2.3.1 “Bandwidth”. The I2C Bus Is Required To Configure Most Of The Camera Sensors.
2.1. Routing MIPI Stream Into CSI-2 The I.MX6 Processors Have One MIPI/CSI-2 Input And Two Parallel Input Interfaces (parallel 0 And Parallel 1; See Figure 2). The Streams In The MIPI Format Jun 4th, 2024
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