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### **Method Of High-Speed Data Acquisition And Continuous Data ...**

The System Is Built On The Altera®R Stratix®R II EP2S60 DSP Development Kit, As Shown In Figure 1. The On-board 12-bit Analog-to-digital Converter (ADC) AD9433 Is Used For The Data Sampling; While The 10/100 Single Chip PHY/MAC SMSC LAN91C111 Is Used For The UDP Communication Betwee Apr 3th, 2024

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1 Castle Point Terrace Hoboken, New Jersey 07030 Yli158,hsun15@stevens.edu Boxiang Dong Montclair State University 1 Normal Ave Montclair, New Jersey 07043 Dongb@montclair.edu Hui (Wendy) Wang Stevens Institute Of Technology 1 Castle Point Terrace Hoboken, New Jersey 07030 Hui.Wang@stevens.edu ABSTRACT Incentivized By The Enormous Economic Pro ... Jun 5th, 2024

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