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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE)
Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2]
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Time-triggered Program Self-monitoring

State In A Periodic Fashion Without Requiring Assistance From An External Monitor Or Internal Timer. We Call This Technique Time-triggered Self-monitoring. First, We Formulate An Opti-mization Problem For Minimizing The Number Of Points In A Program, Where Sel Jun 3th, 2024

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Triple-T (Time-Triggered Transmission) A System Of ...

2 Of 31 Since We Are Dealing With Hard Real-time, It Becomes Essential That Communication Among System Components Also Adheres To Strict Transmission

Times, At Least With Respect To Hard Real-time Functionalities. This Leads To The Topic Of This Paper: Reliable Communication With Guaranteed Transmission Times For Hard Real-time Systems. Feb 2th, 2024

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