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Figure 5 PLC Program De-bugging The Last Set Of  
Exercises In PLC Programming Requires The Students  
To Solve Several Design Problems. The Design  
Problems Use The PLC Test Stand Described In Figures  
1 And 2, Along With Requirements For Timing,  
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Requires 0.64 A, CJ1G Requires 0.91 A, SJ1H Requires  
0.99 A 1 Module Current Consumption Description 1 1  
2 1 3 1 4 1 5 1 6 1 7 1 8 1 9 1 1 Mar 4th, 2024

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