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Vishay Sprague Vs. Vishay BC Components Part Numbers For ...

Cross Reference www.vishay.com Vishay Revision: 23-Aug-12 1 Document Number: 45177 For Technical Questions, Contact: Cmll@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. Jan 10th, 2024

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+1-805-684-8401 - Fax +1-805-684-8402 ... CIRCUIT DIAGRAM VIEWED FROM UNDERSIDE S2 X2 S1 X1 A2 A1 18.5 .73 38.1
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GR6FNA218 Contact Form A Contact Arrangement SPST-NO Voltage Ratings Between Contacts KV Peak 5 Contacts To Coil KV Peak 5 Current Carry Max @ DC Amps 8 @ 30 Mhz Amps 6 Contact Resistance Ohms 0.050 ... Feb 5th, 2024

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Ph.D. In Religious Studies, Southern Methodist University, 1990. Dissertation: Dominant Images For The Church In Augustine's Enarrationes In Psalmos: A Study In Augustine's Ecclesiology. B.A. In Letters, University Of Feb 3th, 2024

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After Verifying The Lead Position Under Fluoroscopy (anterior-posterior And Lateral Views), Compare That Location With The Location That Has The Highest Probability Of Paresthesia Coverage. 8 English 3487A, 3887, 3888 2011-04 2011-04 Filename Date Time UC200xxxxxx EN 4 X 8 Inches (10 Feb 10th, 2024

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MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification For. DEPARTMENT OF DEFENSE STANDARDS MIL-STD-883 - Test Method Standard Microcircuits. MIL-STD-1835 - Interface Standard Electronic Component Case Outlines. DEPARTMENT OF DEFENSE HANDBOOKS MIL-HDBK-103 - List Of Standard Microcircuit Drawings. Feb 3th, 2024

CD4066B CMOS Quad Bilateral Switch Datasheet (Rev. H) - ...

- Analog-to-Digital And Digital-to-Analog Conversions
- Digital Control Of Frequency, Impedance, Phase, And Analog-Signal Gain
- Building Automation 3 Description The CD4066B Device Is A Quad Bilateral Switch Intended For The Transmission Or Multiplexing Of Analog Or Digital Signals. It Is Pin-for-pin Compatible Jan 3th, 2024

74LVC125A Low-Voltage CMOS Quad Buffer

74LVC125A Www.onsemi.com 3 MAXIMUM RATINGS Symbol Parameter Value Condition Unit VCC DC Supply Voltage -0.5 To

+6.5 V V_I DC Input Voltage $-0.5 \leq V_I \leq +6.5$ V V_O DC Output Voltage $-0.5 \leq V_O \leq +6.5$ Output In 3-State V $-0.5 \leq V_O \leq V_{CC} + 0.5$ Output In HIGH Or LOW State (Note 1) V_{IK} DC Input Diode Jan 20th, 2024

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