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Efficient Sorting And Join On NVM-Based Hybrid MemoryEfficient Sorting And Join On NVM-Based Hybrid Memory 19 Table 1. Symbols In DPM Symbol Description Rd Latency Of Read A DRAM Unit Wd Latency Of Write A DRAM Unit Cd Memory Cost Of A Unit If Placed In DRAM Rp Latency Of Read An NVM Unit Wp Latency Of Write An NVM Unit Cp Memory Cost Of A Unit If Place 1th, 2024Advanced Non-Volatile Memories (NVM) - The NASA Electronic ...Advanced Non-Volatile Memories (NVM) Jean Yang -Scharlotta Jean.yang-scharlotta@jpl.nssa.gov 818-354-0412. NASA/ Jet Propulsion Laboratory. Open Access. To Be Presented By Jean Yang- Scharlotta At The NEPP Electronic Technology Workshop, June 26- 29, 2017. 1. Acknowledgment: This Work Was Sponsored By: NASA Office Of Safety & Mission Assurance 2th, 2024NVMe<sup>™</sup> SSD Form Factor Challenges -NVM ExpressNVMe<sup>™</sup> SSD Form Factor Challenges. 2 Agenda Presenters – Bill Lynn Dell/EMC – Michael Krause HPE – Jonathan Hinkle Lenovo – Chris Petersen Facebook – Lee Prewitt Microsoft • A Little Bit Of History • Summary Of Potential NVMe <sup>™</sup> SSD Form Factors ... 1th, 2024.

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