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AN ABSTRACT OF A DISSERTATION ULTRA-WIDEBAND (UWB) IMPULSE ...Cepts And Ideas On UWB Systems. System Level Design, Board Level Design, FPGA Design And

Implementation, And System Integration Are Reported. Two Challenging Issues, The Ultra-high Speed (up To GHz) Connection Between ADC And FPGA And Synchronization For Energy Detection, Have Been Solved. Jan 3th, 2024An 8 GHz Ultra Wideband Transceiver TestbedPerform Time Interleaved Sampling At An Aggregate Rate Of 8 Gsamples/s. The High Sampling Rates Present Extraordinary Demands On The Down-conversion Resources. Samples Are Captured By The Highspeed ADC And Processed Using A Xilinx Virtex-II Pro (XC2VP70) FPGA. The Testbed Has Two Components: A Non Real-time Part Apr 7th, 2024INTRODUCTION TO ULTRA-WIDEBAND ANTENNASGHz Antenna Will Have Approximately 1.67 Inch Elements, A 2-6 GHz Antenna Will Be Half The Size (one Fourth The Area) With About 0.83 Inch Elements, And A 3-9 GHz Antenna Will Be One Third The Size (one Ninth The Area) With Approximately 0.56 Inch Elements. The Antenna Size May Be Scaled To Select Any Particular 3:1 Range Of Desired Frequencies. Jan 18th, 2024. Modified Ultra Wideband (UWB) Antipodal Vivaldi Antenna For 5GRecently, Tapered Slits Antenna/TSA (also Called: Vivaldi Antenna) Has Attracted Attention Due To Their Ultra-wideband Bandwidth, High Gain, And End-fire Radiation Patterns. Vivaldi Antenna Was Firstly Introduced By P. J. Gibson [1]. Feb 10th, 2024A High-Gain

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