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(FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Mar 4th, 2024PROGRAMMING C BCA CODE: BCA -04 I SEMESTER1.6.5 Advantages Of Flowchart 1.6.6 Limitations Of Flowchart 1.7 Pseudocode Example 1.8 Divide And Conquer Strategy 1.8.1 Merge Sort 1.9 Writing Algorithms And Flow Charts For Simple Exercises 1.10 Let Us Sum Up 1.11 Lesson And Activity 1.12 Keywords 1.13 Questions For Discussion Jan 4th, 2024.

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