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Muzakkir Mas'ud Adamu Department Of Computer Engineering, Hussaini Adamu Federal Polytechnic Kazaure, Jigawa State Nigeria. ABSTRACT: A Pulse Width Modulation (PWM) Signal Controller Is Implemented In A Digital Circuit To Control The Speed Of A DC Motor. 2th, 2024.

Digital System Design Using VHDL  
Key Words: Digital Circuit, VHDL, Entity, Architecture. INTRODUCTION VHDL Means VHSIC Hardware Description Language Where VHSIC Stands For Very-High-Speed Integrated Circuit. VHDL Is A Hardware Description Language That Can Be Used To Model A Digital System .VHDL Is Commonly Used As A Design-entry Language For Field-programmable Gate Arrays  
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