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...Key Words: Vedic Formulas, Nikhilam Sutra, Barrel Shifter, Base Selection Module, Propagation Delay, Power Index Determinant. I. INTRODUCTION Arithmetic Operations Like Addition, Subtraction And

Multiplication Are Essential In Different Digital Circuits To Boost The Process Of Computation. Vedic

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Implementation Of FPGA Based 32-Bit Barrel ...1B.Tech Scholar, Dept Of ECE, Vignana Bharathi Institute Of Technology, India, E-mail:

Niranjanreddy469@gmail.com. 2Associate Professor, Dept Of ECE, Vignana Bharathi Institute Of Technology, India, E-mail: Kiranbabus@yahoo.com. Abstract: Barrel Shifter Is A Digital Circuit That Can Shift A Apr 5th, 2024.

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Gert@jhu.edu Received January 25, 2000; Accepted March 21, 2000 Abstract. Feb 6th, 2024VLSI

IMPLEMENTATION OF HASH FUNCTIONS In Addition, The FPGA Synthesis Results Are Presented. From The Synthesis Results It Is Proven That The Proposed System Achieves High Speed Performance. Especially The Throughput Value Is Up To 1,4 And 1,6 Gbps For SHA-1 And RPEMD-160 Respectively. The Proposed System Is Compared With Other Related Works In The Terms Of Jun 2th, 2024 Direct VLSI Implementation Of Combinatorial Algorithms Direct VLSI Implementation Of Combinatorial Algorithms Similarly, A 5 X 5 Matrix Will Fit On A 24 Pin Chip, An 8 X 8 On A 36 Pin Chip, And A 9 X 9 On A 40 Pin Chip. This Is The Limit Of Present Techno May 5th, 2024.

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Abstract. Although Large Numbers Of Hardware And  
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